

# HVD327C

# Variable Capacitance Diode for VHF tuner

REJ03G0218-0200 Rev.2.00 Mar 31, 2006

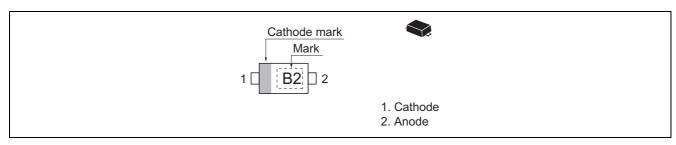
#### **Features**

- Low voltage type (tuning voltage 1 to 10V), it is suitable for ET without DC/DC converter.
- High capacitance ratio (n = 11.0 min).
- Low series resistance and good C-V linearity.
- Super small Flat Lead Package (SFP) is suitable for surface mount design.

### **Ordering Information**

| Type No. | Laser Mark | Package Name | Package Code |  |
|----------|------------|--------------|--------------|--|
| HVD327C  | A4         | SFP          | PUSF0002ZB-A |  |

# **Pin Arrangement**



# **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

| Item                 | Symbol | Value       | Unit |
|----------------------|--------|-------------|------|
| Reverse voltage      | $V_R$  | 15          | V    |
| Junction temperature | Tj     | 125         | °C   |
| Storage temperature  | Tstg   | -55 to +125 | °C   |

#### **Electrical Characteristics**

 $(Ta = 25^{\circ}C)$ 

| Item              | Symbol          | Min  | Тур | Max  | Unit | Test Condition                        |
|-------------------|-----------------|------|-----|------|------|---------------------------------------|
| Reverse current   | I <sub>R1</sub> | _    | _   | 10   | nA   | V <sub>R</sub> = 10 V                 |
|                   | I <sub>R2</sub> | _    | _   | 100  |      | V <sub>R</sub> = 10 V, Ta= 60°C       |
| Capacitance       | C <sub>1</sub>  | 30.5 | _   | 33.5 | pF   | $V_R = 1 V$ , $f = 1 MHz$             |
|                   | C <sub>10</sub> | 2.6  | _   | 2.9  |      | V <sub>R</sub> = 10 V, f = 1 MHz      |
| Capacitance ratio | n               | 11.0 | _   | _    | _    | C <sub>1</sub> /C <sub>10</sub>       |
| Series resistance | r <sub>S</sub>  | _    | _   | 0.8  | Ω    | V <sub>R</sub> = 5 V, f = 470 MHz     |
| Matching error    | ΔC/C *1         | _    | _   | 2.0  | %    | V <sub>R</sub> = 1 to 10 V, f = 1 MHz |

Notes: 1. C.C system (Continuous Connected taping system) enable to make any 10 pcs of ΔC/C continuous in a reel, expect extention to another group. Calculate Matching Error,

$$\Delta \text{C/C} = \frac{(\text{Cmax} - \text{Cmin})}{\text{Cmin}} \times 100 \text{ (\%)}$$

2. For SFP package, the material of lead is exposed for cutting plane. There for, soldering nature of lead tip part is considered as unquestioned. Please kindly consider soldering nature.

### **Main Characteristic**

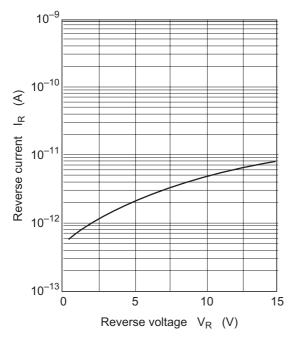


Fig.1 Reverse current vs. Reverse voltage

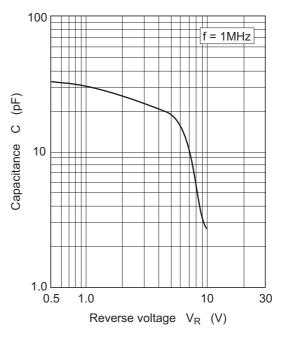


Fig.2 Capacitance vs. Reverse voltage

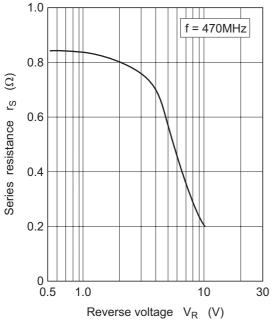
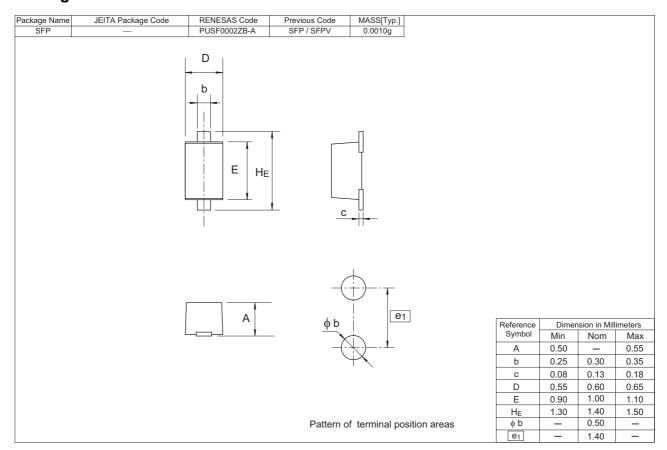


Fig.3 Series resistance vs. Reverse voltage

# **Package Dimensions**



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